W

	Application No.	Applicant(s)
Notice of Allowability	10/742,300	YUAN ET AL.
	Examiner	Art Unit
	Ernest F. Karlsen	2829
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to the amendment of March 3, 2006.		
2. The allowed claim(s) is/are <u>16-20 and 23-25</u> .		
<ul> <li>3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1)  hereto or 2)  to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	5 DNASA ASIMSAMAID	late at Application (DTO 452)
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>		atent Application (PTO-152)
2. Motice of Dranperson's Patent Drawing Review (P10-946)	Paper No /Mail Dat	te
<ul> <li>3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date 0306</li> <li>4.  Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	8), 7. 🛛 Examiner's Amendr	nent/Comment
	9.	ent of Reasons for Allowance
Ernest D. Karsen		
		INEST KARLSEN MARY EXAMINER

U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

## **Examiner's Amendment**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Claims 1-15 have been cancelled for being drawn to non-elected inventions and/or species.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Kim Kanzaki on May 12, 2006.

## Reasons for Allowance

The following is an examiner's statement of reasons for allowance: No reference was found anticipating or a combination of references found making obvious an integrated circuit (IC) chip comprising an IC formed on a substrate, the IC comprising: a first interconnect layer; a first embedded test circuit comprising a first ring oscillator coupled to a first test load, the first test load comprising a programmable interconnect structure formed in the first interconnect layer; and a second embedded test circuit comprising a second ring oscillator, the second ring oscillator comprising an unloaded ring oscillator, and the second ring oscillator being substantially similar to the first ring oscillator.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

Application/Control Number: 10/742,300 Page 3

Art Unit: 2829

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## **Examiner's Comments**

Claims 16-20 and 23-25 are allowed.

Any inquiry concerning this communication should be directed to Ernest F. Karlsen at telephone number 571-272-1961.

Ernest F. Karlsen

May 13, 2006

ERNEST KARLSEN PRIMARY EXAMINER